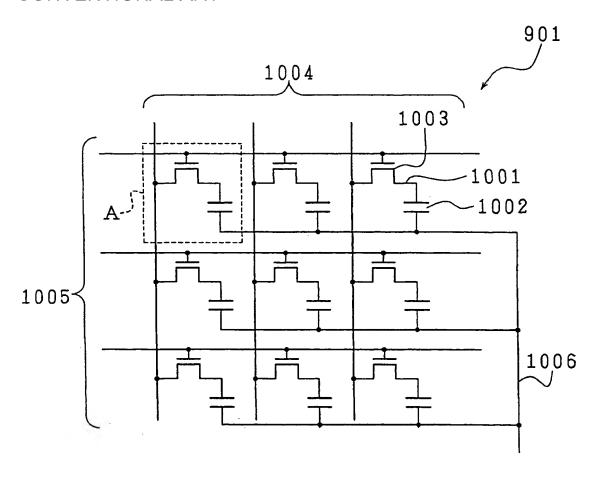
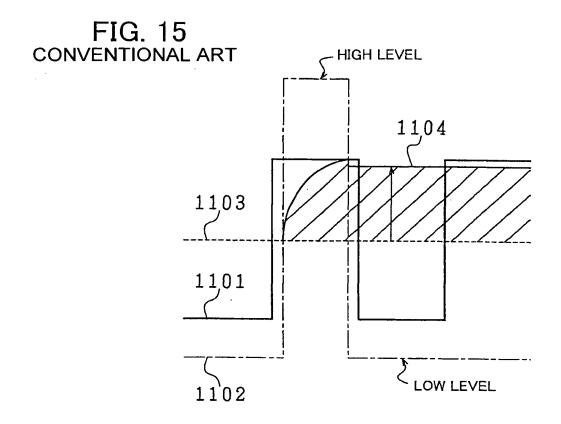
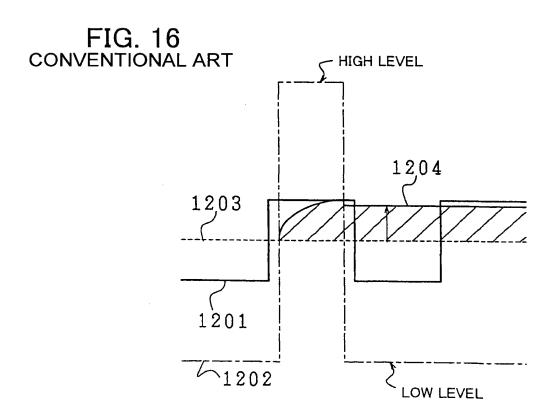


FIG. 14 CONVENTIONAL ART



MAY 2 5 2004 G





MAY 2 5 2004 C.

FIG. 17 CONVENTIONAL ART

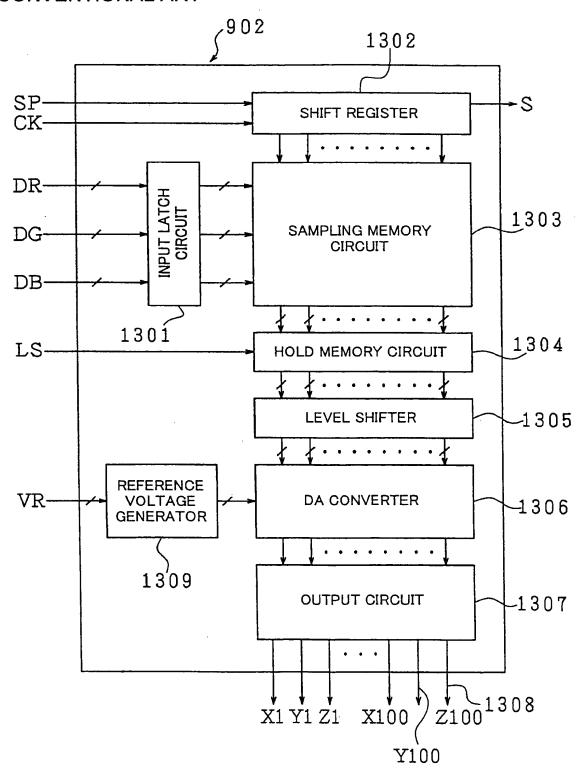
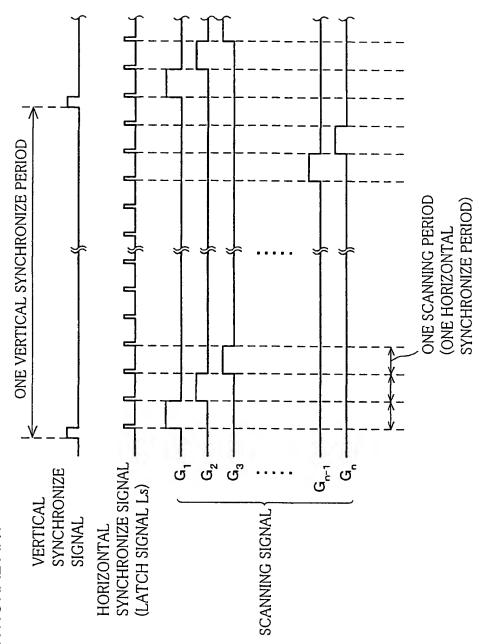






FIG. 18 CONVENTIONAL ART









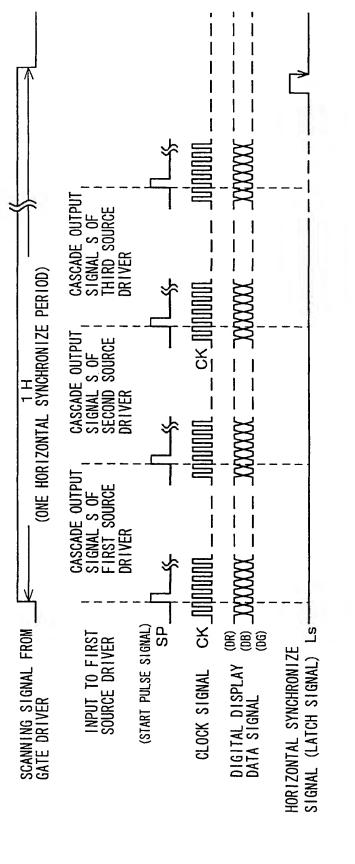


FIG.19 (b) CONVENTIONAL ART

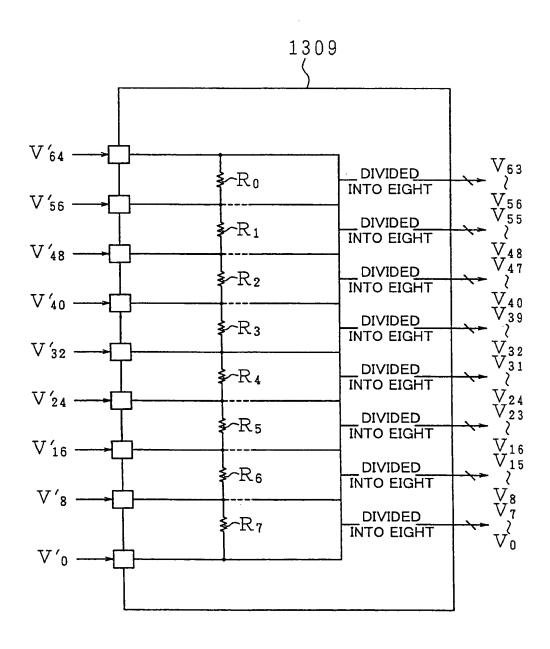
SOURCE DRIVER OUTPUT

DATA HOLD OF THIRD SOURCE DRIVER DATA HOLD OF SECOND SOURCE DRIVER DATA HOLD OF FIRST SOURCE DRIVER

 $X1 \sim X100$ $Y1 \sim Y100$ $Z1 \sim Z100$

MAY 2 5 2004 C.

FIG. 20 CONVENTIONAL ART



MAY 2 5 2004 CS

FIG. 21 CONVENTIONAL ART

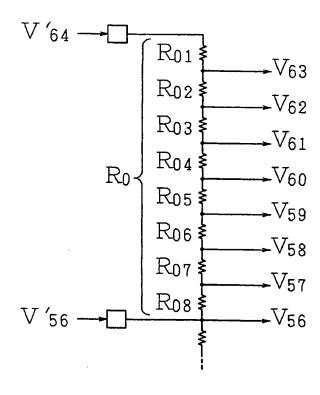




FIG. 22 CONVENTIONAL ART

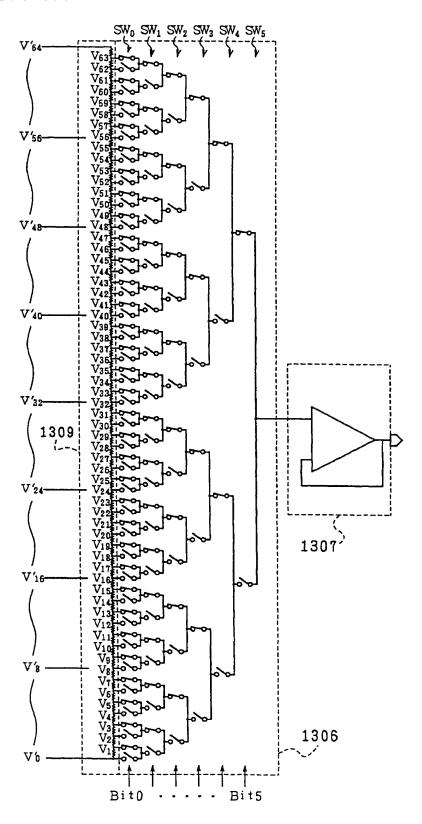
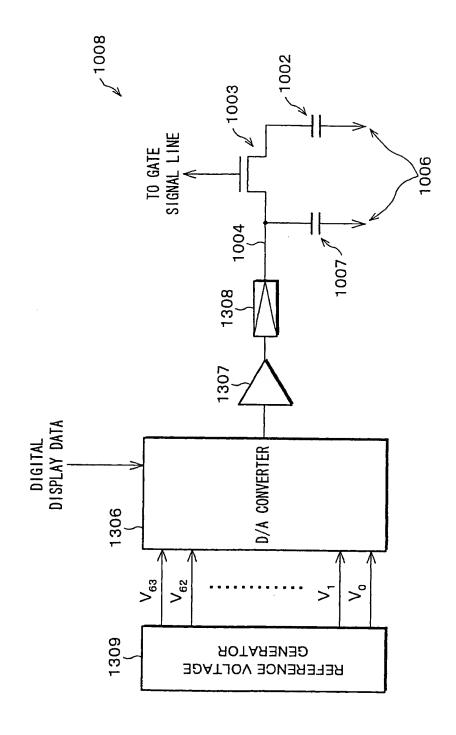


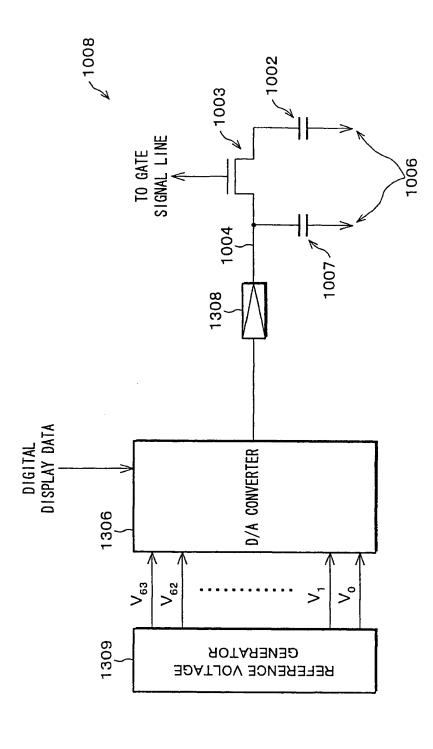


FIG. 23 CONVENTIONAL ART











1002

MAY 2 5 2004

1008 .1003 TO GATE SIGNAL LINE 1006 1004 1308 DIGITAL DISPLAY DATA D/A CONVERTER 1306 1310 1310 1309 REFERENCE VOLTAGE GENERATOR

FIG. 25 CONVENTIONAL ART

REPLACEMENT SHEET

FIG. 26 (a) CONVENTIONAL ART

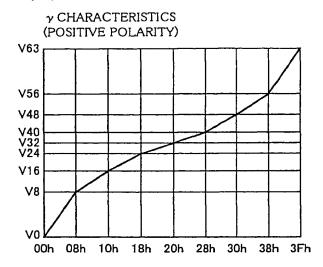


FIG. 26 (b) CONVENTIONAL ART

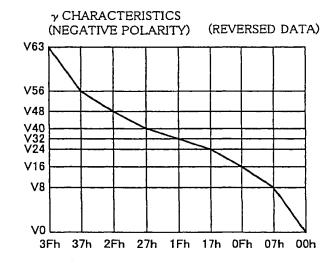


FIG. 26 (c) CONVENTIONAL ART

